

Sincere thanks to Arthur Jackson of ITT Technical Institutes, for pointing out many of these errors.

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| Page 170, Line 5 | 10^{-3} should be 10^3 |
| Page 203, Figure P4.42 | Add a resistor between the collector and +10 V source. Value is 1 k Ω |
| Page 219, bottom equation | Remove the minus sign before the parentheses |
| Page 229, PSPICE listing | In the PSPICE listing at the bottom of this page, the node numbers do not match those of the figure. The simulation will still run correctly. |
| Page 255, Figure 5.27 | Switch labels on two transistors so Q ₁ is on the left. |
| Page 256, last equation on page | There is an “=” sign missing after I _{C1} . Also, on first line of Eq. (5.63), the terms in parentheses are reversed. |
| Page 258, last equation in Example 5.10 | I _C in denominator should be I _{C2} |
| Page 268, Problem 5.23 | Should start out with “In Problem 5.22,” |
| Page 268, Figure P5.25 | Input biasing resistors should be doubled. That is, 45 k Ω should be 90 k Ω and 3 k Ω should be 6 k Ω |
| Page 269, Figure P5.31 | Input biasing resistors should be 10 k Ω each instead of 10 Ω |
| Page 270, Problem 5.33 | Need additional information for part (a). Set V _{BE} =0.7 V and β =100. |
| Page 273 | In Figure P5.48, resistor should be 5k Ω Figure P5.46 is missing. The figure is given at the end of this table. |
| Page 275, Figure P5.54 | Output lead should be moved from collector (+20V) to emitter. |
| Page 299, Equation | Constraint should read v _{DS} =constant |
| Page 324, Figure 6.39(a) | Remove the -V _{SS} connected to the ground line. |
| Page 339, Eq. (6.71) | R _{DD} in numerator should be V _{DD} |
| Page 340, Figure 6.44 | Since this is a common drain amplifier, R _D should be replaced by a short circuit, and the output should be connected to the Source instead of the Drain. |

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| Page 342, Figure 6.45 | The output should be connected to the Source instead of the Drain. |
| Page 354, Problem 6.36 | R_{in} should be R_{source} and also specify $R_1=400\text{ k}\Omega$ |
| Page 382, Problem 7.18 | Reference should be to Figure P7.10 instead of P7.5. |
| Page 879, Figure 16.25 | Second chip should be 74143 instead of 74173. |

